### **18. Popular ConvNets** EECE454 Introduction to Machine Learning Systems

2023 Fall, Jaeho Lee



- 4 lectures ago. Introduced the convolution layer.
  - Parameter-efficient
  - Translation-equivariant (exploits locality)
  - Can handle various resolutions

0	0	0	0	0	0	0
0	60	113	56	139	85	0
0	73	121	54	84	128	0
0	131	99	70	129	127	0
0	80	57	115	69	134	0
0	104	126	123	95	130	0
0	0	0	0	0	0	0

### Recap

Kernel				
0	-1	0		
-1	5	-1		
0	-1	0		

114		

Lee et al., "MVP: An Efficient CNN Accelerator with Matrix, Vector, and Processing-Near-Memory Units," ACM DAES 2022



Lee et al., "MVP: An Efficient CNN Accelerator with Matrix, Vector, and Processing-Near-Memory Units," ACM DAES 2022

### Today

- Popular ConvNet backbones & Important ideas
  - Basic Models
  - Deeper Models
  - Tiny Models
  - Efficiently Scalable Models

(LeNet / AlexNet) (VGG / GoogLeNets / ResNets) (MobileNets / MCUNets) (EfficientNet / NFNets)



### LeNet-5 (1998)

- First practically useful ConvNets
  - Convolutions, then fully-connected layers
  - Pooling after convolution



Fig. 2. Architecture of LeNet-5, a Convolutional Neural Network, here for digits recognition. Each plane is a feature map, i.e. a set of units whose weights are constrained to be identical.

LeCun et al., "Gradient-based learning applied to document recognition," ACM DAES 2022



## **AlexNet (2012)**

- Bigger and deeper version of LeNet
  - 7 hidden layers, 605k neurons, 60 million parameters





## AlexNet (2012)

- Bigger and deeper version of LeNet
  - 7 hidden layers, 605k neurons, 60 million parameters
- Q. Why was bigger & deeper possible?
  - Dataset. ImageNet Large-Scale Visual Recognition Challenge
  - Optimization. Better activation (ReLU)
  - Generalization. Better regularization (Dropout)
  - Computation. Distributed GPU training (two GTX 580)
    - ... let the scale race begin!



### AlexNet (2012)



```
nn.Conv2d(3, 96, kernel_size=11, stride=4, padding=2),
nn.MaxPool2d(kernel_size=3, stride=2),
nn.Conv2d(96, 256, kernel_size=5, padding=2),
nn.MaxPool2d(kernel_size=3, stride=2),
nn.Conv2d(256, 384, kernel_size=3, padding=1),
nn.Conv2d(384, 384, kernel_size=3, padding=1),
nn.Conv2d(384, 256, kernel_size=3, padding=1),
nn.MaxPool2d(kernel_size=3, stride=2),
                                      nn.AdaptiveAvgPool2d((6, 6))
                                      torch.flatten(x, 1)
```



# Deeper Models

Simonyan and Zisserman, "Very deep convolutional networks for large-scale image recognition," ICLR 2015

## VGG (2014)

- Deeper networks
  - up to 19 layers
- Simpler architecture
  - Only 3x3 convolution
    - <u>vs 5x5?</u> less parameters but deeper
  - Only 2x2 pooling
  - No "local response normalization" <sup>conv3</sup>



![](_page_12_Picture_10.jpeg)

### **Stacking Deeper**

### • Key obstacles.

- Gradient vanishing / exploding (no batchnorm back then)
- Too many parameters

![](_page_13_Picture_4.jpeg)

### **Stacking Deeper**

### • Key obstacles.

- Gradient vanishing / exploding (no batchnorm back then)
- Too many parameters

![](_page_14_Figure_4.jpeg)

Figure 1. Training error (left) and test error (right) on CIFAR-10 with 20-layer and 56-layer "plain" networks. The deeper network has higher training error, and thus test error. Similar phenomena on ImageNet is presented in Fig. 4.

He et al., "Deep residual learning for image recognition," CVPR 2016

![](_page_14_Picture_8.jpeg)

### GoogLeNet (2014)

### • Two notable differences

### Inception module. Works as a bottleneck to reduce #channels.

![](_page_15_Figure_3.jpeg)

(a) Inception module, naïve version

Figure 2: Inception module

Szegedy et al., "Going deeper with convolutions," arXiv 2014

(b) Inception module with dimension reductions

![](_page_15_Picture_8.jpeg)

### **GoogLeNet (2014)**

- Two notable differences
  - Inception module.
  - Auxiliary classifier. Resolves vanishing gradient

![](_page_16_Figure_4.jpeg)

Szegedy et al., "Going deeper with convolutions," arXiv 2014

## **GoogLeNet (2014)**

- Two notable differences
  - Inception module.
  - Auxiliary classifier. Resolves vanishing gradient
    - **Note.** Only one FC layer (reduces #params)

![](_page_17_Picture_5.jpeg)

Szegedy et al., "Going deeper with convolutions," arXiv 2014

### **ResNet (2016)**

A more elegant solution to the vanishing gradient.

![](_page_18_Figure_2.jpeg)

He et al., "Deep residual learning for image recognition," CVPR 2016

Figure 2. Residual learning: a building block.

![](_page_18_Picture_8.jpeg)

Veit et al., "Residual Networks Behave Like Ensembles of Relatively Shallow Networks," NeurIPS 2016

## **ResNet (2016)**

 A more elegant solution to the vanishing gradient. • The gradients come from shorter paths

![](_page_19_Figure_3.jpeg)

(a) Conventional 3-block residual network

![](_page_19_Picture_6.jpeg)

(b) Unraveled view of (a)

![](_page_19_Picture_8.jpeg)

### **ResNet (2016)**

### "Bottleneck" blocks

• Known to accelerate the training.

![](_page_20_Figure_3.jpeg)

Figure 5. A deeper residual function  $\mathcal{F}$  for ImageNet. Left: a building block (on  $56 \times 56$  feature maps) as in Fig. 3 for ResNet-34. Right: a "bottleneck" building block for ResNet-50/101/152.

He et al., "Deep residual learning for image recognition," CVPR 2016

![](_page_20_Figure_7.jpeg)

![](_page_20_Picture_8.jpeg)

### **ResNet (2016)**

- Up to over 150 layers in total
  - A better initialization (He init.)
  - Batch normalization after every convolution
  - Dotted line: doubles #channel & downsample by 2

![](_page_21_Figure_5.jpeg)

He et al., "Deep residual learning for image recognition," CVPR 2016

![](_page_21_Picture_8.jpeg)

![](_page_22_Picture_0.jpeg)

![](_page_22_Picture_1.jpeg)

Howard et al., "MobileNets: Efficient Convolutional Neural Networks for Mobile Vision Applications," 2017

## MobileNets (2017, 2018, 2019)

- Less number of parameters for inference on mobile devices
- Key component.
  - Depthwise convolution (v1)
    - Used for Inverted Residual (v2)

### (a) Residual block

![](_page_23_Figure_7.jpeg)

v3 optimized architectures with NAS

### (b) Inverted residual block

![](_page_23_Figure_12.jpeg)

![](_page_23_Picture_13.jpeg)

## MCUNets (2020, 2021, 2022)

- Less memory requirement for training on microcontrollers
- Tools
  - Algorithm/system co-design
  - Neural Architecture Search
  - Better quantization-awareness

![](_page_24_Picture_6.jpeg)

<b>ResNet</b>
Cloud Al

**Memory** (Activation) 16GB

~TB/PB **Storage** (Weights)

Lin et al., "MCUNet: Tiny Deep Learning on IoT Devices," NeurIPS 2020

![](_page_24_Figure_12.jpeg)

<u>MobileNet</u> Mobile Al		<u>MCUNet</u> Tiny Al
4GB —	13,000x smaller	320kB
256GB		1MB

![](_page_24_Picture_14.jpeg)

# **Efficiently Scalable Models**

## **EfficientNets (2019,2021)**

- Q. If we have more budget, how should we increase #param?
  - Increase depth
  - Increase width
  - Less downsampling

![](_page_26_Figure_6.jpeg)

Tan and Le, "Rethinking Model Scaling for Convolutional Neural Networks," ICML 2019

![](_page_26_Figure_8.jpeg)

![](_page_26_Figure_9.jpeg)

Tan and Le, "Rethinking Model Scaling for Convolutional Neural Networks," ICML 2019

v2: + NAS

### **EfficientNets (2019,2021)**

• A. All of them, by some ratio (discovered empirically)

![](_page_27_Figure_3.jpeg)

Figure 2. Model Scaling. (a) is a baseline network example; (b)-(d) are conventional scaling that only increases one dimension of network width, depth, or resolution. (e) is our proposed compound scaling method that uniformly scales all three dimensions with a fixed ratio.

![](_page_27_Picture_6.jpeg)

Brock et al., "High-performance large-scale image recognition without normalization," ICML 2021

## NFNets (2021,2023)

- As of 2023, it seems like *removing batch norms* is a key to build very big convolutional networks
  - transformers...

![](_page_28_Figure_4.jpeg)

• Before this, people thought large convnets cannot work as good as

![](_page_29_Picture_0.jpeg)

### • <u>Next up.</u> Generative models

![](_page_29_Picture_2.jpeg)